

**IPC J-STD-001D Certified IPC Trainer (CIT) Course
Overview & Schedule**

(CIS Course Schedule is provided on the next page)

Hour	Day 1	Day 2	Day 3	Day 4	Day 5
1	S.1 Introductions/Course Overview/Policy and Procedures	S.8 Progress Check & Review	S.13 Progress Check & Review	S.17 Progress Check & Review	S.23 Progress Check & Review
					S.24 Instructor Basics
2	S.1 Continued	S.9 Wire & Terminal Assembly/Soldering Requirements	S.14 TH Demo	S.18 SMT Demo	S.25 Comprehensive Review
	S.2 Safety in Electronic Manufacturing		S.15 T-H Lab	S.19 SMT Lab	
3	S.3 General Requirements of J-STD-001D/Applicable Documents	S.9 Continued	S.15 Continued	S.19 Continued	S.26 Testing
		S.10 Wire & Terminal Demo			
4	S.3 Continued	S.11 Wire/Terminal Lab	S.15 Continued	S.19 Continued	S.26 Continued
	Lunch Break	Lunch Break	Lunch Break	Lunch Break	Lunch Break
5	S.4 Materials, Components & Equipment Requirements	S.11 Continued	S.15 Continued	S.19 Continued	S.26 Continued
6	S.5 General Soldering & Assembly Requirements	S.11 Continued	S.16 Surface Mounting of Components	S.19 Continued	S.26 Continued
7	S.5 Continued	S.11 Continued	S.16 Continued	S.20 Coating and Encapsulation	S.27 Administration of the CIS J-STD001D Program
	S.6 PCB Requirements	S.12 Through-Hole Mounting and Terminations			
8	S.6 Continued	S.12 Continued	S.16 Continued	S.21 Rework & Repair	Graduation
	S.7 Cleaning Process Requirements			S.22 Product Assurance	